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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): A. KURIYAMA, et al.

Group Art Unit: 2812

Serial No.: 10/735,062

Examiner: E. PERT

Filed: December 11, 2003

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

**INFORMATION DISCLOSURE STATEMENT**

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Copy(ies) of these references:

- ☒ Foreign Patent Applications, Foreign Patents and/or Other Non-Patent Documents are attached (Copies of cited U.S. Patents/Publications are not provided).
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Respectfully submitted,  
MORGAN & FINNEGAN, L.L.P.

Dated: February 7, 2006

By: \_\_\_\_\_

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<b>FORM PTO-1449A</b>  <b>INFORMATION DISCLOSURE CITATION</b>	Attorney Docket: 1232-5237	Serial No.: 10/735,062
	Applicant: A. KURIYAMA, et al.	
	Filing Date: December 11, 2003	Group Art Unit: 2812

### U.S. PATENT / PUBLICATION DOCUMENTS

Examiner Initial		Patent/Publication Number	Publication/Issue Date	Name	Filing Date
	1.	US 2005-0053773 A1	03-10-2005	Fukutani	
	2.	US 2004-0043208 A1	03-04-2004	Fukutani	
	3.	US 2004-0048092 A1	03-11-2004	Yasui	
	4.	US 2004-0033339 A1	02-19-2004	Fukutani	
	5.	6,602,620	08-05-2003	Kikitsu	
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### FOREIGN PATENT DOCUMENTS

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	12.	2001-261376 A	09-26-2001	Japan	<input checked="" type="checkbox"/> Yes	Abstract o
	13.	2001-162600	06-19-2001	Japan	<input checked="" type="checkbox"/> Yes	Abstract
	14.	10-321834	12-04-1998	Japan	<input checked="" type="checkbox"/> Yes	Abstract
	15.	H09-157062 A	06-17-1997	Japan	<input checked="" type="checkbox"/> Yes	Abstract
	16.	WO 03/078688	09-25-2003	WIPO	<input checked="" type="checkbox"/> Yes	Abstract
	17.	WO 03/078687	09-25-2003	WIPO	<input checked="" type="checkbox"/> Yes	Abstract
	18.	WO 03/078685	09-25-2003	WIPO	<input checked="" type="checkbox"/> Yes	Abstract
	19.	WO 03/069677	08-21-2003	WIPO	<input checked="" type="checkbox"/> Yes	N/A

Examiner	Date Considered
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<b>FORM PTO-1449B</b> <b>INFORMATION DISCLOSURE</b> <b>CITATION</b>	Attorney Docket: 1232-5237	Serial No.: 10/735,062
	Applicant: A. KURIYAMA, et al.	
	Filing Date: December 11, 2003	Group Art Unit: 2812

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	1.	RC. Furneaux, et al., The formation of controlled-porosity membranes from anodically oxidized aluminum; Nature, Vol. 333, p. 147, (1989)
	2.	Masuda, "Solid-state Physics", Vol. 31, p. 493, (1996)
	3.	X. Duan, et al., Indium Phosphide nanowires as building blocks for nanoscale electronic and optoelectronic devices; Nature, Vol. 409, p. 66, (2001)
	4.	M. Jacobs, et al., Unbalanced magnetron sputtered Si -Al Coatings: plasma conditions and film properties versus sample bias voltage; Surface and Coatings Technology, 116-119 (1999) pp. 735-741
	5.	C. D. Adams, et al., Transition from lateral to transverse phase separation during film co-deposition; Appl. Phys. Lett., 59 (20), 11 November 1991, 2535-2537
	6.	M. Atzmon, et al; Phase separation during film growth; J. Appl. Phys. 72(2), 15 July 1992, 442-446
	7.	C. D. Adams, et al., Monte Carlo simulation of phase separation during thin-film codeposition; J. Appl. Phys. 74 (3), 1 August 1993, 1707-1715
	8.	C. D. Adams, et al; Phase separation during co-deposition of Al-Ge thin films; J. Mat Phys. Vol. 7, No. 3, p. 653-666 (1992)
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